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**1 A reconfigurable FTL (flash translation layer) architecture for NAND flash-based applications**

Chanuk Park, Wonmoon Cheon, Jeonguk Kang, Kangchil Roh, Wonhee Cho, Jin-Sup Kim  
 July 2008 **Transactions on Embedded Computing Systems (TECS)**, Volume 7 Issue 4

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In this article, a novel FTL (flash translation layer) architecture is proposed for NAND flash-based applications such as MP3 players, DSCs (digital still cameras) and SSDs (solid-state drives). Although the basic function of an FTL is to translate a ...

**Keywords:** FTL, Flash memory, performance analysis, reconfigurable architecture

**2 LUSTER: wireless sensor network for environmental research**

L. Saito, A. Wood, Q. Cao, T. Sankar, H. Liu, A. Srinivasan, Y. Wu, W. Kang, J. Stankovic, D. Young, J. Porter  
 November 2007 **Sensys '07: Proceedings of the 5th international conference on Embedded networked sensor systems**

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**Keywords:** LiteTDMA, architecture, environmental science, implementation, mote, network protocol, storage, validation, wireless sensor network

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